

INNOMEDIA ECMM 9500BI EMBEDDED CABLE MODEM MODULE

OVERVIEW

The InnoMedia Embedded Cable Modem Module, ECMM9500BI, is a state-of-the-art DOCSIS 3.0 cable modem with full 8x4 channel bonding support. Up to eight 6/8MHz downstream channels can be bonded with the supported frequency range of 88MHz – 1002MHz (the 88MHz to 108MHz frequency range for DOCSIS 2.0). In addition, four parallel upstream channels can transmit simultaneously meeting the requirements of the DOCSIS 3.0 specification through the use of a wideband DAC, IR filter, and high linearity PGA. By incorporating the latest DOCSIS 3.0 Advanced Time Division Multiple Access (ATDMA) and Synchronous Code Division Multiple Access (SCDMA) technologies, the ECMM 9500 provides up to twice as much upstream bandwidth capability than DOCSIS 2.0 systems. The InnoMedia ECMM 9500 presents an ideal choice as a DOCSIS 3.0 embedded solution for quick time-to-market applications with the perfect blend of flexibility and high performance.

APPLICATIONS

- 1. Embedded Multimedia Terminal Adapter.
- 2. Video Set-top Box.
- 3. Cable Equipment.
- 4. Medical Equipment.
- 5. Cable-based Transponder.
- 6. Industrial-grade Applications

FEATURES

- 1. DOCSIS 1.1, 2.0 and 3.0 compliant.
- 2. Q PSK, 8/16/32/64/128/256 QAM auto detection
- 3. SNMP and IETF cable MIB remote management.
- Integrated ATDMA and SCDMA technology capable of providing up to 120 Mbps upstream data rate and 320 Mbps downstream data rate using 8x4 channel bonding
- 5. Supports up to 32 SIDs (24 UGS)
- 6. Fast installation and easy operation.
- 7. Software upgradeable.
- 8. Self diagnostics.
- 9. Low symmetrical IF output for a direct connection to the channel decoder. RSSI (received strength signal indicator) information through I2C.
- 10. RF splitter for Loop-Through (Optional).



- 11. ROHS product.
- 12. Operational temperature range: -40°C~+85°C.

SPECIFICATIONS

1. Interface

| Item | Specifications |
|---|---|
| Cable connector (See 6.2.2) | F-type or SMB type, female, 75 ohm. |
| GMII for LAN Ethernet port (See 6.2.3) | GMII for 10/100/1000Base-Tx Ethernet port. N-way, Automatic speed negotiation supported. Auto-sensing, auto-detection MDI/ MDI-X. |
| Reset port (See 6.2.1) | Reset to restart/reboot the system when holding the reset longer than 1 second. |
| Console port (See 6.2.1) | Internal Console port. |
| JTAG port (See 6.2.1) | Internal JTAG port. |

2. Cable Specification

Compliant with DOCSIS 3.0/ 2.0/ 1.1 standards.

3. Downstream (Receiver) and Upstream (Transmitter) Characteristics

| Item | Downstream | Upstream | | |
|-------------------|--|---------------------------------|--|--|
| Frequency Range | North America: 88MHz~880MHz | North America: 5MHz~42MHz | | |
| | Europe: 108MHz~1002MHz | Europe: 5MHz~65MHz | | |
| | Japan: 91MHz~1002MHz | Japan: 5MHz~65MHz | | |
| Channel bandwidth | DOCSIS: 6 MHz | TDMA: 200, 400, 800, 1600, 3200 | | |
| | EuroDocsis: 8 MHz | and 6400 kHz | | |
| | | S-CDMA: 1600, 3200 and 6400 kHz | | |
| Impedance | 75 ohm (nominal) | 75 ohm (nominal) | | |
| Modulation | QPSK,64/256QAM | QPSK, 8/16/32/64/128 QAM | | |
| Maximum Data | 320Mbps, 8 channel bonding | 120 Mbps, 4 channel bonding | | |
| Rate | | | | |
| Symbol Rates | 64QAM: 5.057Msym/s | 160, 320, 640, 1280, 2560 and | | |
| | , , | 5120 ksym/s | | |
| | 256QAM: 5.361 Msym/s | | | |
| FEC | RS (128,122) GF128 with Trellis coding | Reed Solomon | | |
| Signal Level | Receive Power Level: –15 dBmV ~ | Transmit Power Level: | | |
| | +15dBmV | TDMA: | | |
| | | +17 ~ +57dBmV(32QAM, 64QAM) | | |
| | | +17 ~ +55dBmV(8QAM, 16QAM) | | |
| | | +17 ~ +61dBmV(QPSK) | | |
| | | SCDMA: | | |
| | | +17~+56dBmV(all modulation) | | |
| Signal-to-Noise | BER < 10^-8 64QAM: > 23.5dB | | | |
| Ratio(SNR) | 256QM: > 30dB | | | |

4. Software Specifications

| Security | DOCSIS Baseline Privacy Plus: 1024-bit RSA and 128-bit Triple-DES for BPKM protocol 56 -bit DES for data encryption X.509 v3 certificates |
|---------------|---|
| DOCSIS | Compliant to DOCSIS 3.0 |
| Protocol | TCP/IP, UDP, ARP, ICMP, DHCP, SNMP, TFTP, TOD, BOOTP, SYSLOG |
| Configuration | Ease of configuration and privacy control provided by resident or |
| | downloaded code from a Cable Modem Termination System (CMTS) |
| Bridging | Support for unicast, broadcast, and multicast IP packets |



| | Variable-length packet cable Media Access Control (MAC) transport layer Mix of contention and reservation-based upstream transmission |
|---|--|
| Quality of Service | Quality of service of MAC layer |
| Management Operations (SNMPv1/v2c/v3) | RFC1157: A simple Network Management Protocol RFC1901: Introduction to Community-based SNMPv2 RFC3416: Version 2 of the Protocol Operation for the SNMP RFC3417: Transport Mapping for the SNMP RFC2578: Structure of Management Information Version 2 RFC2570: Introduction to Version 3 of the internet-standard Network Management RFC3411:An Architecture for Describing SNMP management Frameworks RFC3412: Message Processing and Dispatching for the SNMP RFC3413:SNMP Applications |
| | RFC3414: User-based Security Model (USM) for SNMPv3 RFC3415: View-based Access Control Model (VACM) for SNMP RFC2576: Coexistence between Version 1, Version 2, and Version 3 of the Internet-standard. |
| | Network Management Framework |
| MIBs support | RFC1493: BRIDGE-MIBRFC3418: SNMPv2-MIBRFC2011: IP-MIBRFC2013: UDP-MIBRFC2233: IF-MIBRFC3411: SNMP-FRAMEWORK-MIBRFC3412: SNMP-MPD-MIBRFC3413: SNMP-TARGET-MIB SNMP-NOTIFICATION-MIBRFC3414: SNMP-USER-BASED-SM-MIBRFC3415: SNMP-VIEW-BASED-ACM-MIBRFC2576: SNMP-COMMUNITY-MIBRFC2665: EtherLike-MIBRFC2665: EtherLike-MIBRFC2786: SNMP-USM-DH-OBJECTS-MIBRFC2851: INET-ADDRESS-MIBRFC2083: IOCS-BPI-MIBRFC3083: DOCS-BPI-MIBDRAFT: DOCS-IF-MIBDRAFT: DOCS-IF-MIBDRAFT: DOCS-QOS-MIBAppend L/Annex H: DOCS-CABLE-DEVICE-TRAP-MIB |

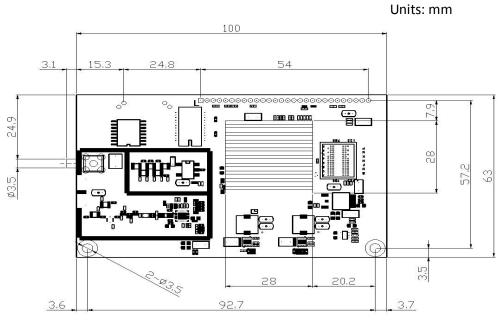
5. Power Consumption and Physical Dimensions

| Item | Specifications | | | |
|-------------------|--|--|--|--|
| Power Consumption | Maximum: 5.64W (12Vdc input/470mA) at upstream power level of | | | |
| | 51dBmv, 8x4 channel bonding, gigabit LAN port, and maximum upstream | | | |
| | and downstream throughputs. | | | |
| | Standby: 5.16W (12Vdc input/430mA) 8x4 channel bonding, gigabit LAN port | | | |
| | (Shall comply with EU CoC spec Tier 2) Comply with Energy Star 2.0) | | | |
| PCB Dimension | • Excluding the RF connector - 100mm x 63mm x 20.3mm (3.94inch x 2.48 inch x 0.8inch) | | | |
| | Including the SMB connector - 103.1mm x 63mm x 20.3mm (4.06inch x 2.48 inch x 0.8inch) | | | |
| Weight | 200grams | | | |

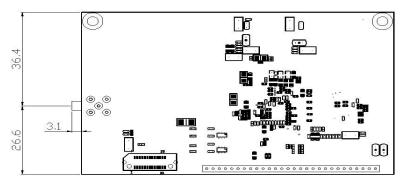


6. Physical Specifications

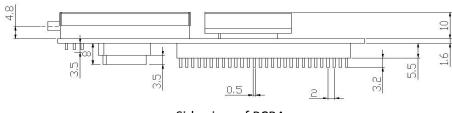
6.1 Dimensions



Top view of PCBA



Bottom view of PCBA





6.2. Pin definition

6.2.1 Main connector: Power and I/O

| Pin | Function | I/O | Pin | Function | I/O |
|-----|--------------------------------|-----|-----|-------------------------------|-----|
| 1 | +3.3Vdc Input | I | 2 | +3.3Vdc Input | Ι |
| 3 | CM Reset | I | 4 | Ground | |
| 5 | EJTAG_TCK | I | 6 | EJTAG_TMS | Ι |
| 7 | EJTAG_TDO | 0 | 8 | EJTAG_TDI | I |
| 9 | EJTAG_RTCK | 0 | 10 | JTAG_EMU0 (for ESBC only) | I/O |
| 11 | JTAG_EMU1 (for ESBC only) | I/O | 12 | JTAG_TRSTN | I |
| 13 | Ground | | 14 | +3.3Vdc Input | I |
| 15 | +3.3Vdc Input | I | 16 | +3.3Vdc Input | I |
| 17 | Ground | | 18 | UART_RD | I |
| 19 | UART_TD | 0 | 20 | Reserved | |
| 21 | Reserved | | 22 | LAN indicator (for ESBC only) | 0 |
| 23 | Upstream indicator (for LED) | 0 | 24 | LAN indicator (for LED) | 0 |
| 25 | On Line indicator (for LED) | 0 | 26 | Power indicator (for LED) | 0 |
| 27 | Downstream indicator (for LED) | 0 | 28 | Ground | |

6.2.2 RF Connectors

| Pin | Function | I/O | Pin | Function | I/O |
|------|-------------------|-----|------|---------------------------------|-----|
| J9-1 | F-Type 75ohm (J9) | I/O | J6-1 | SMB-Type 75ohm (for Japan only) | I/O |

6.2.3 GMII Bus Connector

| Pin | Function | I/O | Pin | Function | I/O |
|-----|----------|-----|-----|---------------------|-----|
| 1 | Ground | | 2 | Ground | |
| 3 | Ground | | 4 | MDI_0+ for Giga PHY | I/O |
| 5 | Ground | | 6 | MDI_0- for Giga PHY | I/O |
| 7 | Ground | | 8 | Ground | |
| 9 | Ground | | 10 | MDI_1+ for Giga PHY | I/O |
| 11 | Ground | | 12 | MDI_1- for Giga PHY | I/O |
| 13 | Ground | | 14 | Ground | |
| 15 | Ground | | 16 | MDI_2+ for Giga PHY | I/O |
| 17 | Ground | | 18 | MDI_2- for Giga PHY | I/O |
| 19 | Ground | | 20 | Ground | |
| 21 | Ground | | 22 | MDI_3+ for Giga PHY | I/O |
| 23 | Ground | | 24 | MDI_3- for Giga PHY | I/O |
| 25 | Ground | | 26 | Ground | |

Revision History



| Version | Date | Contents | |
|---------|------------|--|--|
| 1.0 | 05-15-2012 | First Release | |
| 1.1 | 05-16-2012 | Updated mechanical drawing | |
| 1.2 | 06-05-2012 | Corrected pins 22, 24, and the Ground and Reserve Pin I/O | |
| | | designation in Section 6.2.1 Main Connector. | |
| | | Updated power consumption numbers. | |
| 1.3 | 07-25-2012 | Revised the PCB dimensions and changed the multi power inputs to | |
| | | 3.3Vdc single power input. | |

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